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11/019,28

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- 2) See attached EAST Inventor Search **Printout shows Inventor search terms**

PALM INTRANET

Day: Tuesday Date: 4/10/2007

Time: 10:11:29

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KIM, HYUNG-JIN	SEOUL	KOREA, REPUBLIC OF
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1	US	20060905	Method of	438/592	257/E21.203;	Yoo;
	7101776		fabricating		257/E21.438;	Jae-
	B2		MOS		257/E21.444;	Yoon et
			transistor		257/E21.634;	al.
			using total		257/E21.636;	
			gate		257/E29.161;	
			silicidation		438/595;	
			process		438/655	
1	US	20060801	Bipolar device	438/343	257/E21.379;	Rhee;
	7084041		and method of		257/E29.03;	Hwa-
	B2		manufacturing		257/E29.191;	sung et
			the same		257/E29.193;	al.
			including pre-	ı	438/488;	
			treatment		438/494	
			using germane			
			gas			
1	US	20060425	Method of	438/300	257/E21.409;	Lee;
	7033895	į	fabricating a		257/E21.43;	Seung-
	B2		MOS		257/E29.267;	hwan et
			transistor with		438/184;	al.
			elevated		438/230;	
			source/drain		438/301;	
			structure		438/305;	
			using a		438/306;	
			selective		438/363	
			epitaxial			
			growth			
1	US	20060117	process Multi-layered	257/616	257/190;	Lee; Ho
1	6987310	20000117	structure	257/010	257/191;	et al.
	B2		including an		257/192;	Ct ar.
	DZ		epitaxial layer		257/E21.125;	
			having a low		257/E21.129	
			dislocation		2377321.129	
	\.		defect density,			
			semiconductor			
			device			
	-	1	comprising -			
	-	1	the same, and			1
			method of			
			fabricating the	÷		
			semiconductor			
			device			
1	US	20050816	Apparatus and	318/632	318/556;	Yoo; Ja
	6930462		method for		417/44.1;	Yoo et
	B2	ı	controlling	1	417/44.11;	al.

ð				operation of		417/45;	
			·	compressor		417/53; 62/156; 62/228.1; 62/6	
	1	US 6878575 B2	20050412	Method of forming gate oxide layer in semiconductor devices	438/142	134/2; 134/3; 257/E21.193; 257/E21.268	Yoo; Jae- Yoon et al.
	1	US 6851934 B2	20050208	Stroke control apparatus of reciprocating compressor and method thereof	417/44.11	417/44.1; 417/53	Yoo; Jae-Yoo et al.
	1	US 6835621 B2	20041228	Method of fabricating non-volatile memory device having a structure of silicon-oxidenitride-oxidesilicon	438/261	257/E21.21; 257/E21.268; 257/E21.279; 257/E21.301; 257/E21.423; 257/E21.679; 257/E27.103; 438/287	Yoo; Jae-yoon et al.
	1	US 6815922 B2	20041109	Apparatus and method for controlling operation of compressor	318/632	318/556; 417/44.1; 417/44.11; 417/45; 417/53; 62/156; 62/228.1; 62/6	Yoo; Jae Yoo et al.
	1	US 6685438 B2	20040203	Apparatus and method for controlling operation of reciprocating compressor	417/44.11	417/22; 417/415; 417/44.1; 417/44.8; 417/45; 417/53	Yoo; Jae Yoo et al.
	1	US 6682310 B2	20040127	Apparatus and method for controlling operation of reciprocating motor	417/42	318/687; 417/212; 417/44.1; 417/44.11; 417/45	Yoo; Jae-Yoo et al.

	US 6624496 B2	20030923	Method of forming T-shaped isolation layer, method of forming elevated salicide source/drain region using the same, and semiconductor device having T-shaped isolation layer	257/510	257/E21.549; 257/E21.628; 438/301; 438/427	Ku; Ja- Hum et al.
1	US 6616414 B2	20030909	Apparatus and method for controlling a compressor	417/44.1	62/160	Yoo; Jae Yoo et al.
1	US 6565327 B2	20030520	Circuit for driving linear compressor	417/44.1	417/417; 417/44.11	Yoo; Jae Yoo et al.
1	US 6541953 B2	20030401	Apparatus for detecting shaking of stroke of linear compressor and method therefor	324/76.52	417/44.11	Yoo; Yoo Jae et al.
1	US 6520746 B2	20030218	Apparatus and method for controlling operation of reciprocating compressor	417/44.11	417/18; 417/212; 417/417; 417/44.1; 417/45; 417/53	Yoo; Jae Yoo et al.
1	US 6486039 B2	20021126	Method of fabricating a trench isolation structure having sidewall oxide layers with different thicknesses	438/425	257/E21.548; 257/E21.551; 438/427; 438/433; 438/696; 438/704; 438/717; 438/719; 438/723; 438/724	Yoo; Jae-yoon et al.

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1	US	20020507	Method of	438/296	257/E21.165;	Ahn;
	6383877		forming T-		257/E21.431;	Dong-ho
	B1		shaped		257/E21.438;	et al.
			isolation		257/E21.549;	
			layer, method		257/E21.628;	
			of forming		438/300;	
			elevated		438/301;	
			salicide		438/427;	
			source/drain		438/682	
			region using			
			the same, and			
			semiconductor	·		
			device having			
			T-shaped			
			isolation layer			

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